

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

2. Authorization for this examiner's amendment was given in a telephone interview with Mr. Daniel G. Shanley, Reg. No. 54,863 on 09/20/2010.

3. The claims had been amended as the following:

Claim 1. (Currently Amended) A task execution system including at least two processors, comprising:

a task management table registered with a plurality of tasks, the task management table specifying, for each task in the plurality of tasks, a main execution processor for executing the task and an in-charge-of- stoppage processor for executing the task when the main execution processor stops, wherein each of the plurality of tasks is assigned its corresponding main execution processor and in-charge-of-stoppage processor prior to receiving an instruction to execute the task, whereby no processor other than that assigned as the main execution or in-charge-of-stoppage processor may actually execute the task;

a selecting unit selecting an executable task from among tasks registered in the task management table;

a processor that tries to execute the selected task;

a checking unit checking a stoppage state of a processor registered as the main execution processor for the selected task registered as said main execution processor when the processor that tries to execute the selected task is not registered as the main execution processor for the selected task; and

wherein the processor that tries to execute the selected task ~~a processor registered as the in-charge-of-stoppage processor for the selected task~~ executes the selected task if the processor registered as the main execution processor ~~remains~~ is stopped and the processor that tries to execute the selected task is registered as the in-charge-of-stoppage processor for the selected task.

Claim 2. (Currently Amended) A task execution system including at least two processors, comprising:

a first judging unit judging whether a requested task requested to be registered can be registered as a task of a main execution processor;

second judging unit judging whether the requested task requested to be registered can be registered as a task of an in-charge-of-stoppage processor;

a registering unit registering, prior to receiving an instruction to execute the requested task, an associated relationship between the requested task, the main execution processor, and the in-charge-of-stoppage processor when the requested task can be registered as a task of the main execution processor and the in-charge-of-stoppage processor;

a selecting unit selecting an executable task from among the registered tasks;

a processor that tries to execute the selected task;

a checking unit checking a stoppage state of a processor registered as the main execution processor for the selected task when the processor that tries to execute the selected task is not registered as the main execution processor for the selected task; and

wherein the processor that tries to execute the selected task ~~a processor registered as the in-charge-of-stoppage processor for the selected task~~ executes the selected task if the processor registered as the main execution processor of the selected task ~~remains~~ is stopped and the processor that tries to execute the selected task is registered as the in-charge-of-stoppage processor for the selected task.

Claim 3. Currently Amended) A task execution method in a task execution system including at least two processors, comprising:

selecting an executable task from among tasks registered in a task management table, the task management table being registered with a plurality of tasks and specifying, for each task in the plurality of tasks a main execution processor for executing the task and an in-charge-of-stoppage processor for executing the task when the main execution processor stops, wherein each of the plurality of tasks is assigned its corresponding main execution processor and in-charge-of-stoppage processor prior to receiving an instruction to execute the task, whereby no processor other than that assigned as the main execution or in-charge-of-stoppage processor may actually execute the task;

providing a processor that tries to execute the selected task;

checking a stoppage state of a processor registered as the main execution processor for the selected task when the processor that tries to execute the selected task is not registered as the main execution processor for the selected task; and

executing the selected task if the processor registered as the main execution processor ~~remains~~ is stopped, wherein the selected task is executed by the processor that tries to execute the selected task, and wherein the processor that tries to execute the selected task is a processor registered as the in-charge- of-stoppage processor for the selected task.

Claim 4. (Currently Amended) A program recorded on a computer-storage medium, ~~the program executes~~ executed by a computer thereby performing the steps of:

selecting an executable task from among tasks registered in a task management table, the task management table being registered with a plurality of tasks and specifying, for each task in the plurality of tasks, a main execution processor for executing the task and an in-charge-of-stoppage processor for executing the task when said the main execution processor stops, wherein each of the plurality of tasks is assigned its corresponding main execution processor and in-charge-of-stoppage processor prior to receiving an instruction to execute the task;

providing a processor that tries to execute the selected task;

checking a stoppage state of a processor registered as the main execution processor for the selected task when the processor that tries to execute the selected task is not registered as the main execution processor for the selected task; and

executing the selected task if the processor registered as the main execution processor is stopped, wherein the selected task is executed by the processor that tries to execute the selected

Art Unit: 2195

task, and wherein the processor that tries to execute the selected task is a processor registered as the in-charge- of-stoppage processor for the selected task.

Claim 5. (Currently Amended) A personal digital assistant comprising ~~the~~ a program of ~~claim 4~~ that when executed perform the steps of:

selecting an executable task from among tasks registered in a task management table, the task management table being registered with a plurality of tasks and specifying, for each task in the plurality of tasks, a main execution processor for executing the task and an in-charge-of-stoppage processor for executing the task when said the main execution processor stops, wherein each of the plurality of tasks is assigned its corresponding main execution processor and in-charge-of-stoppage processor prior to receiving an instruction to execute the task;

providing a processor that tries to execute the selected task;

checking a stoppage state of a processor registered as the main execution processor for the selected task when the processor that tries to execute the selected task is not registered as the main execution processor for the selected task; and

executing the selected task if the processor registered as the main execution processor is stopped, wherein the selected task is executed by the processor that tries to execute the selected task, and wherein the processor that tries to execute the selected task is registered as the in-charge- of-stoppage processor for the selected task.

Claim 6. (Currently Amended) A personal computer comprising ~~the~~ a program of ~~claim 4~~ that when executed perform the steps of:

selecting an executable task from among tasks registered in a task management table, the task management table being registered with a plurality of tasks and specifying, for each task in the plurality of tasks, a main execution processor for executing the task and an in-charge-of-stoppage processor for executing the task when said the main execution processor stops, wherein each of the plurality of tasks is assigned its corresponding main execution processor and in-charge-of-stoppage processor prior to receiving an instruction to execute the task;

providing a processor that tries to execute the selected task;

checking a stoppage state of a processor registered as the main execution processor for the selected task when the processor that tries to execute the selected task is not registered as the main execution processor for the selected task; and

executing the selected task if the processor registered as the main execution processor is stopped, wherein the selected task is executed by the processor that tries to execute the selected task, and wherein the processor that tries to execute the selected task is registered as the in-charge-of-stoppage processor for the selected task.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer N. To whose telephone number is (571) 272-7212. The examiner can normally be reached on M-T 6AM- 3:30 PM, F 6AM- 2:30 PM.

5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Meng-Ai An can be reached on (571) 272-3756. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2195

6. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

/Lewis A. Bullock, Jr./
Supervisory Patent Examiner, Art Unit 2193

/Jennifer N To/
Patent Examiner, Art Unit 2195